

FORM PTO-1449 (modified)
To: U.S. Department of Commerce
(PW FORM PAT-1449)
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Atty.
Dkt. No.

M#

Client Ref.

81674

308127

Applicant: KUMAR, et al.

Appln. No.: N/A 10/787,231

Filing Date: N/A 4/26/04

Examiner: N/A

Group Art Unit: N/A

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

Date: February 26, 2004

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of

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U.S. PATENT DOCUMENTS

Examiner's Initials*		Document Number	Date MM/YYYY	Name (Family Name of First Inventor)	Class	Sub Class	Filing Date (if appropriate)
RJP	AR	2001/0045815A1	11/29/01	Muratov et al.			
	BR	6,437,548	08/2002	Aas et al.	323	272	
	CR	6,181,120	01/2001	Hawkes et al.	323	282	
	DR	6,177,783	01/2001	Donohue, John E.	323	272	
	ER	6,160,386	12/2000	Hemena et al.	323	272	
	FR	6,130,526	10/2000	Yang et al.	323	272	
QJ	GR	6,009,000	12/1999	Siri, Kasemsan	363	21.09	
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	IR						
	JR						

FOREIGN PATENT DOCUMENTS

		Document Number	Date MM/YYYY	Country	Inventor Name	English Abstract	Translation Readily Available
						Enclosed	No
RJP	KR	0073970A1	12/28/00	WIPO			X
	LR						
	MR						
	NR						
	OR						

OTHER (including in this order: Author, Title, Periodical Name, Date, Pertinent Pages, etc.):

RJP	PR	"GENERALIZED SYNTHESIS AND APPLICATIONS OF ZERO-IMPEDANCE CONVERTER AND ITS DUAL," Novica A. Losic, et. al., IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS 1: FUNDAMENTAL THEORY AND APPLICATIONS, IEEE INC., NEW YORK, U.S. Vol. 46, No. 11, November 1999, pgs. 1349-1359, XP000931899, ISSN: 1057 - 7122			
	QR				
	RR				
	SR				
	TR				

Examiner

Date Considered: 3/29/06

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.